

Application Note:

Interfacing non-standard cameras to Matrox Genesis

DALSA CT-P1-2048W (PIRANHA)

June 15, 1999

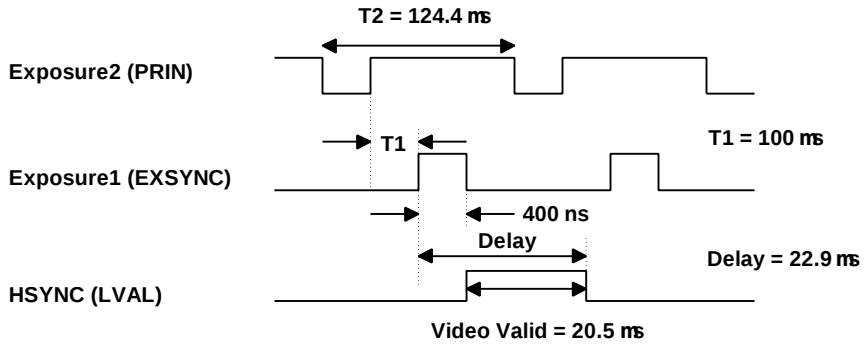
Camera Descriptions	• 2048 × 8-bit. • Quad channel LVDS digital video. • External synchronization required. • Exposure control. • Maximum data rate: 25 MHz per output.
Interface modes	• Fixed line scan rate, variable line scan
Camera Interface Briefs	Mode 1: Fixed line scan rate • 2048 × 8-bit. • Quad channel LVDS digital video. • DCF configured for 512 lines per virtual frame. • Line scan rate is fixed and determined by EXPOSURE1 (EXSYNC) signal frequency. • Matrox Genesis sending EXPOSURE1 (EXSYNC) and EXPOSURE2 (PRIN) signals to camera; the EXPOSURE1 (EXSYNC) signal controls line readout and EXPOSURE2 (PRIN) signal controls exposure time. • Matrox Genesis receiving PIXEL CLOCK (STROBE @ 25 MHz), HSYNC (LVAL) and video signals from camera; a high LVAL signal indicates valid pixels. • DCF used: CTP1DEL3.DCF Mode 2: Variable line scan rate • 2048 × 8-bit. • Quad channel LVDS digital video. • DCF configured for 512 lines per virtual frame. • Line scan rate is variable and controlled by external trigger signal. • Matrox Genesis receiving TTL external trigger. • Matrox Genesis sending EXPOSURE1 (EXSYNC) and EXPOSURE2 (PRIN) signals to camera; the EXPOSURE1 (EXSYNC) signal controls line readout and EXPOSURE2 (PRIN) signal controls exposure time. • Matrox Genesis receiving PIXEL CLOCK (STROBE @ 25 MHz), HSYNC (LVAL) and video signals from camera; a high LVAL signal indicates valid pixels. • DCF used: CTP1DAE3.DCF * Matrox Genesis Main Board and Genesis-LC with PNS # 5245 and 5246 respectively ** Matrox LVDS Digital Data Input Board

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Camera Interface Details	Mode 1: Fixed line scan rate mode - Matrox Genesis sends the EXPOSURE1 (EXSYNC) signal to the camera; the camera awaits the rising edge of the signal and after a short (constant) delay initiates line readout. Note that this camera model features an inverted output from the right tap. Additional processing time is thereby required to invert the mirrored output. Line rate: The EXPOSURE2 (PRIN) period in the DCF specifies the line rate of the camera. The EXPOSURE2 (PRIN) period is currently set to 3110 pixels. With a 25 MHz pixel clock, this translates to an 8.0 kHz line rate. Virtual frame rate: The virtual frame rate for the current settings of the DCF is 15.7 Hz. Exposure time: The time between the rising edge of the EXPOSURE2 (PRIN) and EXPOSURE1 (EXSYNC) signals is the exposure time. The default exposure time for this DCF is 100 μs. In order to select the exposure time, the width and deployment time of EXPOSURE1 (EXSYNC) and EXPOSURE2 (PRIN) must be set in Matrox Intellicam. The exposure time of the camera can be modified in the DCF using Matrox Intellicam, Genesis Native Library function imCamControl() or with the MIL digitizer control function MdigControl(). Refer to the appropriate manual or user guide for additional information. Maximum / minimum exposure time: Since the Matrox Genesis timer is 16-bit wide, the maximum exposure time is calculated to be $65536/25 \text{ MHz} = \mathbf{2.62 \text{ ms}}$. For proper operation, the exposure signal must remain inactive for a minimum of 6 clock pulses before being asserted. Therefore the minimum exposure time is 240 ns. Smallest exposure time increment: The pixel clock is the reference clock that the exposure time is being set by. The smallest increment of the exposure time is 40 ns.  Mode 2 : Variable line scan rate - Once it has received the external trigger signal, Matrox Genesis sends the EXPOSURE2 (PRIN) signal to the camera to initiate exposure. Matrox Genesis will send the EXPOSURE1 (EXSYNC) signal to the camera following a delay that is equal to the desired exposure time. A short (constant) delay will follow after receiving the EXPOSURE1 (EXSYNC), followed by the camera sending the HSYNC (LVAL) signal to the Matrox Genesis to initiate line readout. Note that this camera model features an inverted output from the right tap. Additional processing time is thereby required to invert the mirrored output.
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Camera Interface Details (continued)	• Line and virtual frame rate: Line and virtual frame rates are variable and controlled by the frequency of the external trigger signal. • Maximum exposure time, Minimum exposure time, and Smallest exposure time increments: are the same as for Mode 1: <i>Fixed line scan rate</i>																																																																																																																																																																													
Cabling Requirements	Mode 1: Fixed line scan rate • DBHD100-TO-OPEN cable and GEN/DIG/BRD/L/_ board required for digital data, synchronization and control signals. • Connections between the 37-pin connector (OS1/OS2) of the camera and the 100-pin connector of the GEN-DIG-BRD/L/_ are as follows: <table> <tr> <th colspan="2">DALSA CT-P1-2048W (37-pin connector – OS1/OS2)</th><th></th><th colspan="2">GEN-DIG-BRD/L/_ (100-pin connector)</th></tr> <tr> <th><i>Pin name</i></th><th><i>Pin no.</i></th><th></th><th><i>Pin name</i></th><th><i>Pin no.</i></th></tr> <tr><td>D0</td><td>16</td><td>→</td><td>DATA, INPUT, 0+</td><td>01</td></tr> <tr><td>D0B</td><td>35</td><td>→</td><td>DATA, INPUT, 0-</td><td>02</td></tr> <tr><td>D1</td><td>15</td><td>→</td><td>DATA, INPUT, 1+</td><td>03</td></tr> <tr><td>D1B</td><td>34</td><td>→</td><td>DATA, INPUT, 1-</td><td>04</td></tr> <tr><td>D2</td><td>14</td><td>→</td><td>DATA, INPUT, 2+</td><td>05</td></tr> <tr><td>D2B</td><td>33</td><td>→</td><td>DATA, INPUT, 2-</td><td>06</td></tr> <tr><td>D3</td><td>13</td><td>→</td><td>DATA, INPUT, 3+</td><td>07</td></tr> <tr><td>D3B</td><td>32</td><td>→</td><td>DATA, INPUT, 3-</td><td>08</td></tr> <tr><td>D4</td><td>12</td><td>→</td><td>DATA, INPUT, 4+</td><td>09</td></tr> <tr><td>D4B</td><td>31</td><td>→</td><td>DATA, INPUT, 4-</td><td>10</td></tr> <tr><td>D5</td><td>11</td><td>→</td><td>DATA, INPUT, 5+</td><td>11</td></tr> <tr><td>D5B</td><td>30</td><td>→</td><td>DATA, INPUT, 5-</td><td>12</td></tr> <tr><td>D6</td><td>10</td><td>→</td><td>DATA, INPUT, 6+</td><td>13</td></tr> <tr><td>D6B</td><td>29</td><td>→</td><td>DATA, INPUT, 6-</td><td>14</td></tr> <tr><td>D7</td><td>09</td><td>→</td><td>DATA, INPUT, 7+</td><td>15</td></tr> <tr><td>D7B</td><td>28</td><td>→</td><td>DATA, INPUT, 7-</td><td>16</td></tr> <tr><td>D0</td><td>08</td><td>→</td><td>DATA, INPUT, 8+</td><td>17</td></tr> <tr><td>D0B</td><td>27</td><td>→</td><td>DATA, INPUT, 8-</td><td>18</td></tr> <tr><td>D1</td><td>07</td><td>→</td><td>DATA, INPUT, 9+</td><td>19</td></tr> <tr><td>D1B</td><td>26</td><td>→</td><td>DATA, INPUT, 9-</td><td>20</td></tr> <tr><td>D2</td><td>06</td><td>→</td><td>DATA, INPUT, 10+</td><td>21</td></tr> <tr><td>D2B</td><td>25</td><td>→</td><td>DATA, INPUT, 10-</td><td>22</td></tr> <tr><td>D3</td><td>05</td><td>→</td><td>DATA, INPUT, 11+</td><td>23</td></tr> <tr><td>D3B</td><td>24</td><td>→</td><td>DATA, INPUT, 11-</td><td>24</td></tr> <tr><td>D4</td><td>04</td><td>→</td><td>DATA, INPUT, 12+</td><td>25</td></tr> <tr><td>D4B</td><td>23</td><td>→</td><td>DATA, INPUT, 12-</td><td>26</td></tr> <tr><td>D5</td><td>03</td><td>→</td><td>DATA, INPUT, 13+</td><td>27</td></tr> <tr><td>D5B</td><td>22</td><td>→</td><td>DATA, INPUT, 13-</td><td>28</td></tr> <tr><td>D6</td><td>02</td><td>→</td><td>DATA, INPUT, 14+</td><td>29</td></tr> <tr><td>D6B</td><td>21</td><td>→</td><td>DATA, INPUT, 14-</td><td>30</td></tr> <tr><td>D7</td><td>01</td><td>→</td><td>DATA, INPUT, 15+</td><td>31</td></tr> <tr><td>D7B</td><td>20</td><td>→</td><td>DATA, INPUT, 15-</td><td>32</td></tr> </table> continued				DALSA CT-P1-2048W (37-pin connector – OS1/OS2)			GEN-DIG-BRD/L/_ (100-pin connector)		<i>Pin name</i>	<i>Pin no.</i>		<i>Pin name</i>	<i>Pin no.</i>	D0	16	→	DATA, INPUT, 0+	01	D0B	35	→	DATA, INPUT, 0-	02	D1	15	→	DATA, INPUT, 1+	03	D1B	34	→	DATA, INPUT, 1-	04	D2	14	→	DATA, INPUT, 2+	05	D2B	33	→	DATA, INPUT, 2-	06	D3	13	→	DATA, INPUT, 3+	07	D3B	32	→	DATA, INPUT, 3-	08	D4	12	→	DATA, INPUT, 4+	09	D4B	31	→	DATA, INPUT, 4-	10	D5	11	→	DATA, INPUT, 5+	11	D5B	30	→	DATA, INPUT, 5-	12	D6	10	→	DATA, INPUT, 6+	13	D6B	29	→	DATA, INPUT, 6-	14	D7	09	→	DATA, INPUT, 7+	15	D7B	28	→	DATA, INPUT, 7-	16	D0	08	→	DATA, INPUT, 8+	17	D0B	27	→	DATA, INPUT, 8-	18	D1	07	→	DATA, INPUT, 9+	19	D1B	26	→	DATA, INPUT, 9-	20	D2	06	→	DATA, INPUT, 10+	21	D2B	25	→	DATA, INPUT, 10-	22	D3	05	→	DATA, INPUT, 11+	23	D3B	24	→	DATA, INPUT, 11-	24	D4	04	→	DATA, INPUT, 12+	25	D4B	23	→	DATA, INPUT, 12-	26	D5	03	→	DATA, INPUT, 13+	27	D5B	22	→	DATA, INPUT, 13-	28	D6	02	→	DATA, INPUT, 14+	29	D6B	21	→	DATA, INPUT, 14-	30	D7	01	→	DATA, INPUT, 15+	31	D7B	20	→	DATA, INPUT, 15-	32
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D5	03	→	DATA, INPUT, 13+	27																																																																																																																																																																										
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Cabling Requirements (continued)	DALSA CT-P1-2048W (37-pin connector – OS1/OS2)		GEN-DIG-BRD/L/_ (100-pin connector)	
	STROBE	17	→	CLOCK, INPUT, + 39
	STROBEB	36	→	CLOCK, INPUT, - 40
	LVAL	18	→	HSYNC, INPUT, + 33
	LVALB	37	→	HSYNC, INPUT, - 34
	• Connections between the 37-pin connector (OS3/OS4) of the camera and the 100-pin connector of the GEN-DIG-BRD/L/_ are as follows:			
	DALSA CT-P1-2048W (37-pin connector – OS3/OS4)		GEN-DIG-BRD/L/_ (100-pin connector)	
	<i>Pin name</i>	<i>Pin no.</i>	<i>Pin name</i>	<i>Pin no.</i>
	D0	08	→	DATA, INPUT, 16+ 51
	D0B	27	→	DATA, INPUT, 16- 52
	D1	07	→	DATA, INPUT, 17+ 53
	D1B	26	→	DATA, INPUT, 17- 54
	D2	06	→	DATA, INPUT, 18+ 55
	D2B	25	→	DATA, INPUT, 18- 56
	D3	05	→	DATA, INPUT, 19+ 57
	D3B	24	→	DATA, INPUT, 19- 58
	D4	04	→	DATA, INPUT, 20+ 59
	D4B	23	→	DATA, INPUT, 20- 60
	D5	03	→	DATA, INPUT, 21+ 61
	D5B	22	→	DATA, INPUT, 21- 62
	D6	02	→	DATA, INPUT, 22+ 63
	D6B	21	→	DATA, INPUT, 22- 64
	D7	01	→	DATA, INPUT, 23+ 65
	D7B	20	→	DATA, INPUT, 23- 66
	D0	16	→	DATA, INPUT, 24+ 67
	D0B	35	→	DATA, INPUT, 24- 68
	D1	15	→	DATA, INPUT, 25+ 69
	D1B	34	→	DATA, INPUT, 25- 70
	D2	14	→	DATA, INPUT, 26+ 71
	D2B	33	→	DATA, INPUT, 26- 72
	D3	13	→	DATA, INPUT, 27+ 73
	D3B	32	→	DATA, INPUT, 27- 74
	D4	12	→	DATA, INPUT, 28+ 75
	D4B	31	→	DATA, INPUT, 28- 76
	D5	11	→	DATA, INPUT, 29+ 77
	D5B	30	→	DATA, INPUT, 29- 78
	D6	10	→	DATA, INPUT, 30+ 79
	D6B	29	→	DATA, INPUT, 30- 80
	D7	09	→	DATA, INPUT, 31+ 81
	D7B	28	→	DATA, INPUT, 31- 82

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Cabling Requirements (continued)	Connections between the 15-pin female connector (Control) of the camera and the 100-pin connector on the GEN-DIG-BRD/L/_ are as follows:			
	DALSA CT-P1-2048W		GEN-DIG-BRD/L/_	
	(15-pin connector – Control)		(100-pin connector)	
	<i>Pin name</i>	<i>Pin no.</i>	<i>Pin name</i>	<i>Pin no.</i>
	EXSYNC	12	EXPOSURE1, OUTPUT, +	95
	EXSYNCB	04	EXPOSURE1, OUTPUT, -	96
	PRIN	05	EXPOSURE2, OUTPUT, +	97
	PRINB	13	EXPOSURE2, OUTPUT, -	98
	Connections between the 15-pin male power connector of the camera and the power supply are as described in the DALSA camera manual.			
	Mode 2: Variable line scan rate			
	- DBHD100-TO-OPEN and IMG-7W2-TO-5BNC cables, and GEN/DIG/BRD/L/_ board required for TTL external trigger, digital data, synchronization and control signals. - TTL external trigger source should be connected to the TTL trigger input of IMG-7W2-TO-5BNC cable. - All other connections are as in Mode 1: <i>Fixed line scan rate</i>			

The DCF(s) mentioned in this application note can be found on the MIL and Native Library CD, or our FTP site ([ftp.matrox.com](ftp:matrox.com)). The information furnished by Matrox Electronics System, Ltd. is believed to be accurate and reliable. Please verify all interface connections with camera documentation or manual. Contact your local sales representative or Matrox Sales office or Matrox Imaging Applications at 514-822-6061 for assistance.

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