GENESIS

PULNIX TM-1001

February 9, 2001

Camera Descriptions	 1024 × 1024 × 8-bit @ 15fps. Single channel RS-422 digital or analog video output. Progressive scan. Internal or external exposure control. Pixel clock rate: 20 MHz 					
Interface modes	Continuous, asynchronous reset (pulse width control mode)					
Camera Interface Briefs	Mode 1: Continuous (analog)					
	 *Matrox Genesis main board with grab module 1008 × 1018 × 8-bit @ 15fps. Single channel analog video. Progressive scan. Continuous video. Matrox Genesis receiving video signals from camera. DCF used: TM1001N.DCF 					
	Mode 2: Continuous (digital)					
	PULNIX TM-1001 VIDEO LVD FVD CLK Matrox Genesis (GEN/////STD*+ GEN-DIG-BRD/R/_**)					
	 997 × 1016 × 8-bit. Single channel RS-422 digital video. Progressive scan. Continuous video. *Matrox Genesis main board with grab module ** Matrox RS-422 digital data input board 					
	 Matrox Genesis receiving HSYNC (LDV), VSYNC (FDV), PIXEL CLOCK (CLK @ 20 MHz) and video signals from camera. DCF used: TM1001C.DCF 					

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Camera	Mode 3: Asynchronous Reset (Pulse Width Control Mode)						
Interface							
Briefs			Matroy Co				
(continued)	DUUDIN	FVD	Matrox Ge				
		СLК	(GEN/_/_/)	A PARTY			
	, TM-1001		GEN-DIG-BR	Philes)			
		TTL EXTERNAL TRIGGER					
	$0.07 \times 1016 \times 9.14$		*Matrox Genesis main bo ** Matrox RS-422 digital	5			
	• $997 \times 1016 \times 8$ -bit.		0	•			
	• Single channel RS-422 d	igital video.					
	Progressive scan						
		Matrox Genesis receiving TTL external trigger signal.					
	 Matrox Genesis sending EXPOSURE2 (VINIT) signal to camera: the EXPOSURE2 both initiates exposure and controls exposure time. 						
		•					
		• Matrox Genesis receiving HSYNC (LDV), VSYNC (FDV), PIXEL CLOCK (CLK @ 20 MHz), and video signals from camera.					
	• DCF used: TM1001A.DO						
	• DCI used. INITOOTA.DO						
	Modes 1 and 2: Continu	10us (analog, digital)					
Camera							
Interface Details	• Frame rate: Matrox Genesis receives the continuous video from the camera at 15 frames						
Detalls	per second.						
	• Exposure time: Exposure time is inversely proportionate to the frame rate (no shutter) or determined by the shutter setting. Pafer to the semare manual for more information						
	determined by the shutter setting. Refer to the camera manual for more information.						
	• Camera switch settings: Refer to the camera manual for additional information. Switches for this mode should be set as follows:						
		Modes 1 and 2: Continue	ous				
		Switches	Settings				
		Shutter (Speed)	Any except 9				
		NRM / ASY	NRM				
		DSP / NSP	NSP				
	Mode 3: Asynchronous l	Reset (Pulse Width Co	ntrol Mode)				
	• Once it has received the	external trigger signal, I	Matrox Genesis sends th	e EXPOSURE2			
	(VINIT) signal to the camera with a width equal to the desired exposure.						
	• Frame rate: The frame rate is determined by the frequency of the external trigger signal.						
	• Exposure time: The act						
	the exposure time. The d	-	-				
	to change the width and Sottings monu tob in M						
	more information.	arrox interincam. Const	in the matrox intellicat	in User Guide for			
	more information.	gs menu tab in Matrox Intellicam. Consult the Matrox Intellicam User Guide for nformation.					

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ntarfaga	for this mode should be	e set as follows:					
Interface Details (continued)	Mode 3: Asynchronous Reset						
		Switches		Settings			
		Shutter (Speed)		9			
		NRM / ASY		ASY			
		DSP / NSP		NSP			
Cabling Requirements	Mode 1: Continuous (a • IMG-7W2-TO-5BNC c	0.	ideo outp	ut of camera.			
(equirements	• Video input BNC of IMG-7W2-TO-5BNC cable should be connected to VIDEO OUT BNC connector of camera.						
	Mode 2: Continuous (d	ligital)					
	• DBHD100-TO-OPEN cable and GEN/DIG/BRD/R/_ board required for digital data, synchronization and control signals.						
	Connections between the GEN-DIG-BRD/R/_ are GEN-DIG-BRD/R/_ (100-pin connector) Pin name	-	r of the ca	Amera and the 100-pi PULNiX TM-100 (31-pin connector Pin name)1		
	CLOCK, INPUT, +	39	,	CLK+	01		
	CLOCK, INPUT, -	40	\leftarrow	CLK-	17		
	HSYNC, INPUT, +	33	\leftarrow	LVD+	02		
	HSYNC, INPUT, -	33	\leftarrow	LVD-	18		
		35	\leftarrow	EVD- FVD+	03		
	VSYNC, INPUT, +	36	\leftarrow	FVD-	19		
	VSYNC, INPUT, -		\leftarrow				
		50			04		
	GROUND	50 37		GND	16		
	GROUND	37	,	GND	16 08		
	GROUND DATA, INPUT, 0+	37 01	~	GND D0+	08		
	GROUND DATA, INPUT, 0+ DATA, INPUT, 0-	37 01 02	\leftarrow	GND D0+ D0-	08 24		
	GROUND DATA, INPUT, 0+ DATA, INPUT, 0- DATA, INPUT, 1+	37 01 02 03	$\stackrel{\leftarrow}{\leftarrow}$	GND D0+ D0- D1+	08 24 09		
	GROUND DATA, INPUT, 0+ DATA, INPUT, 0- DATA, INPUT, 1+ DATA, INPUT, 1-	37 01 02 03 04	$\leftarrow \leftarrow \leftarrow$	GND D0+ D0- D1+ D1-	08 24 09 25		
	GROUND DATA, INPUT, 0+ DATA, INPUT, 0- DATA, INPUT, 1+ DATA, INPUT, 1- DATA, INPUT, 2+	37 01 02 03 04 05	$\downarrow \downarrow \downarrow \downarrow$	GND D0+ D0- D1+ D1- D2+	08 24 09 25 10		
	GROUND DATA, INPUT, 0+ DATA, INPUT, 0- DATA, INPUT, 1+ DATA, INPUT, 1- DATA, INPUT, 2+ DATA, INPUT, 2-	37 01 02 03 04 05 06	$\downarrow \downarrow \downarrow \downarrow \downarrow \downarrow$	GND D0+ D0- D1+ D1- D2+ D2-	08 24 09 25 10 26		
	GROUND DATA, INPUT, 0+ DATA, INPUT, 0- DATA, INPUT, 1+ DATA, INPUT, 1- DATA, INPUT, 2+ DATA, INPUT, 2- DATA, INPUT, 3+	37 01 02 03 04 05 06 07	$\uparrow \uparrow \uparrow \uparrow \uparrow \uparrow \uparrow$	GND D0+ D0- D1+ D1- D2+ D2- D3+	08 24 09 25 10 26 11		
	GROUND DATA, INPUT, 0+ DATA, INPUT, 0- DATA, INPUT, 1+ DATA, INPUT, 1- DATA, INPUT, 2+ DATA, INPUT, 2- DATA, INPUT, 3+ DATA, INPUT, 3-	37 01 02 03 04 05 06 07 08	1 1 1 1 1 1 1	GND D0+ D0- D1+ D1- D2+ D2- D3+ D3-	08 24 09 25 10 26 11 27		
	GROUND DATA, INPUT, 0+ DATA, INPUT, 0- DATA, INPUT, 1+ DATA, INPUT, 1- DATA, INPUT, 2+ DATA, INPUT, 2- DATA, INPUT, 3+	37 01 02 03 04 05 06 07	$\uparrow \uparrow \uparrow \uparrow \uparrow \uparrow \uparrow$	GND D0+ D0- D1+ D1- D2+ D2- D3+	08 24 09 25 10 26 11		

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Cabling Requirements (continued)	GEN-DIG-BRD/R/_ (100-pin connector) <i>Pin name</i>	Pin no.		PULNiX TM-1001 (31-pin connector) <i>Pin name</i>	Pin no.		
(•••••••)	DATA, INPUT, 5+	11	\leftarrow	D5+	13		
	DATA, INPUT, 5-	12	\leftarrow	D5-	29		
	DATA, INPUT, 6+	13	\leftarrow	D6+	14		
	DATA, INPUT, 6-	14	\leftarrow	D6-	30		
	DATA, INPUT, 7+	15	\leftarrow	D7+	15		
	DATA, INPUT, 7-	16	\leftarrow	D7-	31		
	EXPOSURE2, OUTPUT, TTL	88*	\rightarrow	VINIT	20*		
	* This connection is not required fo	* This connection is not required for this mode, however allows this cable to be used with both digital modes.					
	Mode 3: Asynchronous Reset (Pulse Width Control Mode)						
	 DBHD100-TO-OPEN cable and GEN/DIG/BRD/R/_ board required for digital data, synchronization and control signals. The connections between the 100-pin connector of the GEN-DIG-BRD/R/_ and the 31-pin connector of the camera are as in Mode 2: <i>Continuous mode (digital)</i> with the exception of the following additional connection: 						
	GEN-DIG-BRD/R/_ (100-pin connector) Pin name		n no.	PULNiX TM-1001 (31-pin DC connector) Pin name	Pin no.		
	EXPOSURE2, OUTPUT,	TTL 88	\rightarrow	VINIT	20		
	• TTL external trigger source should be connected to the TTL Trigger Input of the IMG- 7W2-TO-5BNC cable.						
	• To use an RS-422 external trigger input, modify the DCF using Matrox Intellicam (refer to the Matrox Intellicam User Guide for more information), and add the following connections between the 100-pin connector of the GEN-DIG-BRD/R/_ and the external trigger source:						
	GEN-DIG-BRD/R/_ (100-pin connector)		E	xternal trigger source			
	Pin name Pi	in no.	Pi	n name			
	TRIGGER, INPUT, +	47 ←	- "F	RS-422 TRIGGER+"			
	TRIGGER, INPUT, -	10					
		48 ←	- "F	RS-422 TRIGGER-"			

The DCF(s) mentioned in this application note can be found on the MIL and Native Library CD, or our FTP site (ftp.matrox.com). The information furnished by Matrox Electronics System, Ltd. is believed to be accurate and reliable. Please verify all interface connections with camera documentation or manual. Contact your local sales representative or Matrox Sales office or Matrox Imaging Applications at 514-822-6061 for assistance.

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