

Application Note:

Interfacing non-standard cameras to Matrox Genesis

DALSA CL-CB-1024W

May 21, 1999

Camera Descriptions	<ul style="list-style-type: none"> • 1024 × 8-bit. • Single channel RS-422 digital video. • External synchronization required. • Exposure control. • Maximum data rate: 20 MHz.
Interface modes	<ul style="list-style-type: none"> • Fixed line scan rate, variable line scan rate (binning, no binning)
Camera Interface Briefs	<p>Mode 1: Fixed line scan rate</p> <div data-bbox="430 609 1494 840"> </div> <ul style="list-style-type: none"> • 1024 × 8-bit (no binning), 512 × 8-bit (binning). • Single channel RS-422 digital video. • DCF configured for 512 lines per virtual frame. • Line scan rate is fixed and determined by the frequency of the EXPOSURE1 (EXSYNC) signal. • Matrox Genesis sending EXPOSURE1 (EXSYNC) and EXPOSURE2 (PRIN) signals to camera; the EXPOSURE1 (EXSYNC) signal controls line readout and EXPOSURE2 (PRIN) signal controls exposure time. • Matrox Genesis receiving PIXEL CLOCK (STROBE @ 20 MHz - no binning, 10 MHz - binning), HSYNC (LVAL), and video signals from camera; a high LVAL signal indicates valid pixels. • DCF used: CLCBDEL5.DCF (no binning) • DCF used: CLCBDEL6.DCF (binning) <p><small>*Matrox Genesis Main Board with Grab Module **Matrox RS-422 Digital Data Input Board</small></p> <p>Mode 2: Variable line scan rate</p> <div data-bbox="430 1386 1494 1638"> </div> <ul style="list-style-type: none"> • 1024 × 8-bit (no binning), 512 × 8-bit (binning). • Single channel RS-422 digital video. • DCF configured for 512 lines per virtual frame. • Line scan rate is variable and controlled by external trigger signal. • Matrox Genesis receiving TTL external trigger. <p><small>*Matrox Genesis Main Board with Grab Module **Matrox RS-422 Digital Data Input Board</small></p> <p>(Briefs continued)</p>

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Camera Interface Briefs (continued)	<ul style="list-style-type: none"> • Matrox Genesis sending EXPOSURE1 (EXSYNC) and EXPOSURE2 (PRIN) signals to camera; the EXPOSURE1 (EXSYNC) signal controls line readout and EXPOSURE2 (PRIN) signal controls exposure time. • Matrox Genesis receiving PIXEL CLOCK (STROBE @ 20 MHz - no binning, 10 MHz - binning), HSYNC (LVAL), and video signals from camera; a high LVAL signal indicates valid pixels. • DCF used: CLCBDAE5.DCF (no binning) • DCF used: CLCBDAE6.DCF (binning)
Camera Interface Details	<p>Mode 1: Fixed line scan rate mode</p> <ul style="list-style-type: none"> • Matrox Genesis sends the EXPOSURE1 (EXSYNC) signal to the camera; the camera awaits the rising edge of the signal and after a short (constant) delay initiates line readout. • Line rate: The EXPOSURE2 (PRIN) period in the DCF specifies the line rate of the camera. The EXPOSURE2 (PRIN) period is currently set to 3110 pixels. With a 20 MHz pixel clock, this translates to a 6.4 kHz line rate (6.2 kHz for binning). • Virtual frame rate: The virtual frame rate for the current settings of the DCF is 12.5 Hz (12.1 Hz for binning). • Exposure time: The time between the rising edge of the EXPOSURE2 (PRIN) and EXPOSURE1 (EXSYNC) signals is the exposure time. The default exposure time for this DCF is 100 μs. In order to select the exposure time, the width and deployment time of each EXPOSURE1 (EXSYNC) and EXPOSURE2 (PRIN) must be set in Matrox Intellicam. The exposure time of the camera can be modified in the DCF using Matrox Intellicam, Genesis Native Library function imCamControl() or with the MIL digitizer control function MdigControl(). Refer to the appropriate manual or user guide for additional information. • Maximum / minimum exposure time: Since the Matrox Genesis timer is 16-bit wide, the maximum exposure time is calculated to be $65536/20 \text{ MHz} = 3.27 \text{ ms}$ (6.55 ms for binning). For proper operation, the exposure signal must remain inactive for a minimum of 6 clock pulses before being asserted. Therefore the minimum exposure time is 300 ns (600 ns for binning). <div data-bbox="500 1459 1404 1795"> <p style="text-align: center;">T2 = 155 ns (161 ns*)</p> <p>Exposure2 (PRIN)</p> <p style="text-align: center;">T1 = 100 ns **</p> <p>Exposure1 (EXSYNC)</p> <p style="text-align: center;">500 ns (1 ms*)</p> <p style="text-align: center;">Delay</p> <p>HSYNC (LVAL)</p> <p style="text-align: center;">Delay = 53.5 ns (54.1 ms*)</p> <p style="text-align: center;">Video Valid = 51.2 ns **</p> <p>* values are for binning ** same for both</p> </div>

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<div>Camera Interface Details (continued)</div>	<div><ul style="list-style-type: none">• Smallest exposure time increment: The pixel clock is the reference clock that the exposure time is being set by. The smallest increment of the exposure time is 50 ns (100 ns for binning).<div>Mode 2 : Variable line scan rate</div><ul style="list-style-type: none">• Once it has received the external trigger signal, Matrox Genesis sends the EXPOSURE2 (PRIN) signal to the camera to initiate exposure. Matrox Genesis will send the EXPOSURE1 (EXSYNC) signal to the camera following a delay that is equal to the desired exposure time. A short (constant) delay will follow after receiving the EXPOSURE1 (EXSYNC), followed by the camera sending the HSYNC (LVAL) signal to the Matrox Genesis to initiate line readout.• Line and virtual frame rate: Line and virtual frame rates are variable and controlled by the frequency of the external trigger signal.• Maximum exposure time, Minimum exposure time, and Smallest exposure time increments: are the same as for Mode 1: <i>Fixed line scan rate</i></div>																																																																																
<div>Cabling Requirements</div>	<div><div>Mode 1: Fixed line scan rate</div><ul style="list-style-type: none">• DBHD100-TO-OPEN cable and GEN/DIG/BRD/R/_ board required for digital data, synchronization and control signals.• Connections between the 25-pin connector (OS1) of the camera and the 100-pin connector of the GEN-DIG-BRD/R/_ are as follows:<table><tr><th colspan="2">DALSA CL-CB-1024W (25-pin connector - OS1)</th><th></th><th colspan="2">GEN-DIG-BRD/R/_ (100-pin connector)</th></tr><tr><th>Pin name</th><th>Pin no.</th><th></th><th>Pin name</th><th>Pin no.</th></tr><tr><td>D7 (MSB)</td><td>01</td><td>→</td><td>DATA, INPUT, 7+</td><td>15</td></tr><tr><td>D7B</td><td>14</td><td>→</td><td>DATA, INPUT, 7-</td><td>16</td></tr><tr><td>D6</td><td>02</td><td>→</td><td>DATA, INPUT, 6+</td><td>13</td></tr><tr><td>D6B</td><td>15</td><td>→</td><td>DATA, INPUT, 6-</td><td>14</td></tr><tr><td>D5</td><td>03</td><td>→</td><td>DATA, INPUT, 5+</td><td>11</td></tr><tr><td>D5B</td><td>16</td><td>→</td><td>DATA, INPUT, 5-</td><td>12</td></tr><tr><td>D4</td><td>04</td><td>→</td><td>DATA, INPUT, 4+</td><td>09</td></tr><tr><td>D4B</td><td>17</td><td>→</td><td>DATA, INPUT, 4-</td><td>10</td></tr><tr><td>D3</td><td>05</td><td>→</td><td>DATA, INPUT, 3+</td><td>07</td></tr><tr><td>D3B</td><td>18</td><td>→</td><td>DATA, INPUT, 3-</td><td>08</td></tr><tr><td>D2</td><td>06</td><td>→</td><td>DATA, INPUT, 2+</td><td>05</td></tr><tr><td>D2B</td><td>19</td><td>→</td><td>DATA, INPUT, 2-</td><td>06</td></tr><tr><td>D1</td><td>07</td><td>→</td><td>DATA, INPUT, 1+</td><td>03</td></tr><tr><td>D1B</td><td>20</td><td>→</td><td>DATA, INPUT, 1-</td><td>04</td></tr></table><div>continued</div></div>	DALSA CL-CB-1024W (25-pin connector - OS1)			GEN-DIG-BRD/R/_ (100-pin connector)		Pin name	Pin no.		Pin name	Pin no.	D7 (MSB)	01	→	DATA, INPUT, 7+	15	D7B	14	→	DATA, INPUT, 7-	16	D6	02	→	DATA, INPUT, 6+	13	D6B	15	→	DATA, INPUT, 6-	14	D5	03	→	DATA, INPUT, 5+	11	D5B	16	→	DATA, INPUT, 5-	12	D4	04	→	DATA, INPUT, 4+	09	D4B	17	→	DATA, INPUT, 4-	10	D3	05	→	DATA, INPUT, 3+	07	D3B	18	→	DATA, INPUT, 3-	08	D2	06	→	DATA, INPUT, 2+	05	D2B	19	→	DATA, INPUT, 2-	06	D1	07	→	DATA, INPUT, 1+	03	D1B	20	→	DATA, INPUT, 1-	04
DALSA CL-CB-1024W (25-pin connector - OS1)			GEN-DIG-BRD/R/_ (100-pin connector)																																																																														
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Cabling Requirements (continued)	DALSA CL-CB-1024W (25-pin connector – OS1)			GEN-DIG-BRD/R/_ (100-pin connector)	
	<i>Pin name</i>	<i>Pin no.</i>		<i>Pin name</i>	<i>Pin no.</i>
	D0	08	→	DATA, INPUT, 0+	01
	D0B	21	→	DATA, INPUT, 0-	02
	STROBE	09	→	CLOCK, INPUT, -	39
	STROBEB	22	→	CLOCK, INPUT, +	40
	LVAL+	10	→	HSYNC, INPUT, +	33
	LVAL-	23	→	HSYNC, INPUT, -	34
	• Connections between the 15-pin female connector (Control) of the camera and the 100-pin connector of the GEN-DIG-BRD/R/_ are as follows:				
	DALSA CL-CB-1024W (15-pin connector)			GEN-DIG-BRD/R/_ (100-pin connector)	
	<i>Pin name</i>	<i>Pin no.</i>		<i>Pin name</i>	<i>Pin no.</i>
	EXSYNC	12	←	EXPOSURE1, OUTPUT, +	95
	EXSYNCB	04	←	EXPOSURE1, OUTPUT, -	96
	PRIN	05	←	EXPOSURE2, OUTPUT, +	97
	PRINB	13	←	EXPOSURE2, OUTPUT, -	98
BIN	09	←	USER, OUTPUT, 1+	93	
BINB	01	←	USER, OUTPUT, 1-	94	
• Connections between the 15-pin male power connector of the camera and the power supply are as described in the DALSA camera manual.					
Mode 2: Variable line scan rate					
• DBHD100-TO-OPEN and IMG-7W2-TO-5BNC cables, and GEN/DIG/BRD/R/_ board required for external trigger, digital data, synchronization and control signals.					
• TTL external trigger source should be connected to the TTL trigger input of IMG-7W2-TO-5BNC cable.					
• All other connections are as in Mode 1: <i>Fixed line scan rate</i>					

The DCF(s) mentioned in this application note can be found on the MIL and Native Library CD, or our FTP site ([ftp.matrox.com](ftp:ftp.matrox.com)). The information furnished by Matrox Electronics System, Ltd. is believed to be accurate and reliable. Please verify all interface connections with camera documentation or manual. Contact your local sales representative or Matrox Sales office or Matrox Imaging Applications at 514-822-6061 for assistance.

Corporate Headquarters:
Canada and U.S.A.
Matrox Electronic Systems
Ltd.
1055 St.Regis Blvd.
Dorval, Quebec, Canada
H9P 2T4
Tel: (514) 685-7230
Fax: (514) 822-6273

